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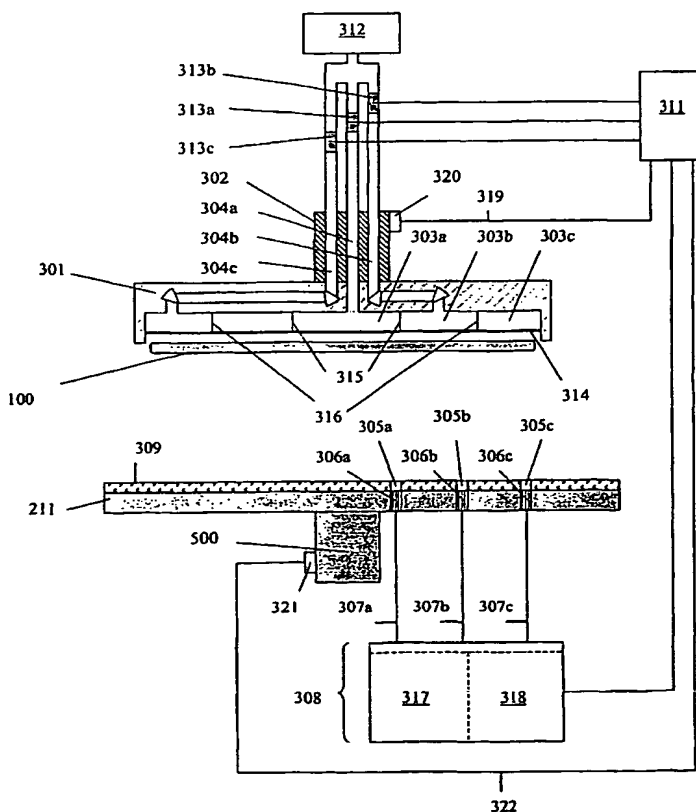
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(54) Title: MULTIPROBE DETECTION SYSTEM FOR CHEMICAL-MECHANICAL PLANARIZATION TOOL



(57) Abstract: The invention is a method and apparatus for planarizing a wafer. Discrete measurements are taken across the surface of the wafer at a desired spatial density. The measurements may be generated using a flash lamp to reflect a light signal off the surface of the wafer with a spectrometer analyzing the reflected light. A plurality of probes may be used at different locations to shorten the time necessary for taking measurements across the full front surface of the wafer and for allowing a plurality of areas to be sampled substantially simultaneously. A control system receives the measurements and their corresponding locations. The control system is then able to analyze the data looking for areas or bands on the front surface of the wafer that need an increase or decrease in material removal rate. The control system is then able to adjust one or more planarization parameters to improve the process for the current wafer or for future wafers.

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MULTIPROBE DETECTION SYSTEM FOR CHEMICAL-MECHANICAL PLANARIZATION TOOL

Technical Field

The present invention relates generally to semiconductor manufacturing and more specifically to monitoring in situ a planarization process of a wafer with a plurality of probes and feeding back the information to improve the planarization process.

Background of the Invention

A flat disk or "wafer" of single crystal silicon is the basic substrate material in the semiconductor industry for the manufacture of integrated circuits. Semiconductor wafers are typically created by growing an elongated cylinder or boule of single crystal silicon and then slicing individual wafers from the cylinder. The slicing causes both faces of the wafer to be extremely rough. The front face of the wafer on which integrated circuitry is to be constructed must be extremely flat in order to facilitate reliable semiconductor junctions with subsequent layers of material applied to the wafer. Also, the material layers (deposited thin film layers usually made of metals for conductors or oxides for insulators) applied to the wafer while building interconnects for the integrated circuitry must also be made a uniform thickness.

Integrated circuits manufactured today are made up of literally millions of active devices such as transistors and capacitors formed in a semiconductor substrate. Integrated circuits rely upon an elaborate system of metalization in order to connect the active devices into functional circuits. A typical multilevel interconnect 100 is shown in Fig. 1. Active devices such as MOS transistors 107 are formed in and on a silicon substrate or well 102. An interlayer dielectric (ILD) 104, such as SiO_2 , is formed over silicon substrate 102. ILD 104 is used to electrically isolate a first level of metalization that is typically aluminum (Al), with copper (Cu) increasing in popularity, from the active devices formed in substrate 102. Metalized contacts 106 electrically couple active devices formed in substrate 102 to interconnections 108 of the first level of metalization. In a similar manner metal vias 112 electrically couple interconnections 114 of a second level of metalization to interconnections 108 of the first level of metalization. Contacts 106 and vias 112 typically comprise a metal 116 such as tungsten (W) surrounded by a barrier metal 118 such as titanium-nitride (TiN). Additional ILD/contact and metalization layers may be stacked one upon the other to achieve the desired interconnections. The ILD/contact and metalization layers may be formed, for example, using a dual damascene process.

Planarization is the process of removing projections and other imperfections to create a flat planar surface, both locally and globally, and/or the removal of material to create a uniform thickness for a deposited thin film layer on a wafer. Semiconductor wafers are planarized or polished to achieve a substantially smooth, flat finish before performing process steps that create the integrated circuitry or interconnects on the wafer. A considerable amount of effort in the manufacturing of modern complex, high density multilevel interconnects is devoted to the planarization of the individual layers of the interconnect structure. Nonplanar surfaces create poor optical resolution of subsequent photolithography processing steps. Poor optical resolution prohibits the printing of high-density lines. Another problem with nonplanar surface topography is the step coverage of subsequent metalization layers. If a step height is too large there is a serious danger that open circuits will be created. Planar surface layers are required in the fabrication of modern high-density integrated circuits. To this end, chemical-mechanical planarization (CMP) tools have been developed to provide controlled planarization of both structured and unstructured wafers.

CMP consists of a chemical process and a mechanical process acting together, for example, to reduce height variations across a dielectric region, clear metal deposits in damascene processes or remove excess oxide in shallow trench isolation fabrication. The chemical-mechanical process is achieved with a liquid medium containing chemicals that react with the front surface of the wafer when it is mechanically stressed during the planarization process.

In a conventional CMP tool for planarizing a wafer, a wafer is secured in a carrier connected to a shaft. The shaft is typically connected to mechanical means for transporting the wafer between a load or unload station and a position adjacent to a polishing pad mounted to a rigid or flexible platen or supporting surface. Pressure is exerted on the back surface of the wafer by the carrier in order to press the front surface of the wafer against the polishing pad, usually in the presence of slurry. The wafer and/or polishing pad are then moved in relation to each other via motor(s) connected to the shaft and/or supporting surface in order to remove material in a planar manner from the front surface of the wafer.

It is often desirable to monitor the front surface of the wafer during the planarization process. One known method is to use an optical system that monitors the front surface of the wafer in situ by positioning an optical probe under the polishing pad. Laser interferometry, signal template matching and multifrequency analysis techniques, as well as others, are known monitoring methods. The signal from the probe may be transmitted and received through an

opening in the polishing pad. The opening in the polishing pad may be filled with an optically transparent material, or "window", in order to prevent polishing slurry or other contaminants from being deposited into the probe and obscuring the optical path to the wafer. The data from the optical system is typically analyzed by a control system to determine the current condition of the front surface of the wafer. It is possible to terminate the planarization process (call end-point) once the front surface of the wafer has reached a desired condition. An optical system may be used to compensate for drifts in the planarization process, variability in the associated consumables (polishing pads and slurries), and variability in the thickness of incoming wafers.

A reliable end-point detection system is critical for maintaining an optimum CMP process. The end-point system detects the point in the planarization process when the overburden being polished is removed everywhere across the wafer. Excessive removal of overburden from the front surface of the wafer, whether a raw sheet film, or an STI, metal or dielectric layer structure on the front wafer surface, may damage the wafer. CMP of copper will become one of the most common and critical planarization processes when the copper interconnect technology starts to dominate the fabrication of integrated circuits. Fig. 2 illustrates some of the potential problems if excessive overburden is removed, in this case copper, from the front surface of a wafer. At time T1 a layer of deposited copper 200 remains on the wafer. The CMP planarization process should terminate at time T2 or just slightly thereafter for an optimum planarized surface. However, if the planarization process is not terminated quickly enough, excessive removal of copper in the interconnects 202 may occur as shown at time T3. The dishing of the copper interconnects 202 occurs since the copper is softer than the silicon dioxide 201 and is therefore removed at a faster rate.

End-point detection and monitoring is required for copper CMP due to the variations in the incoming thickness distribution of the copper film as well as microstructural variations in the deposited copper film. These result in nonuniform clearing of the copper across the front surface of the wafer. Several problems exist with conventional in situ monitoring techniques that limit their ability to accurately detect the clearing of the copper.

Some conventional monitoring systems use a laser interferometer positioned below a rotating polishing pad. However, this type of monitoring system can only take measurements while the laser and optical path through the polishing pad are in alignment with the front surface of the wafer. This situation creates a very narrow time-period during each rotation of the polishing pad that measurements can be taken. The narrow time-period and the delay between time-periods for taking measurements created by the rotating polishing pad greatly

diminish the capabilities of the monitoring system.

In addition, some conventional systems tend to measure a relatively large spot, or integrate a number of large spots (smear measurement), on the wafer's surface to increase the surface area of the wafer being monitored. However, these measurements create a system with
5 very poor sensitivity that cannot distinguish high metalization density from mere residual metal. Improved sensitivity is required to detect residuals on the wafer's surface that are on the order of the spot size, but may influence the quality of the planarization process.

Applicants also noticed a further problem with some conventional monitoring systems in that they do not associate a measurement with the location on the wafer where the
10 measurement was taken. This prevents in situ corrections to the planarization process based on information related to particular areas of the wafer.

Another problem Applicants noticed was that some conventional monitoring systems do not take measurements in fine enough increments along a radial line leaving concentric bands between measurements unmonitored. Problems in the planarization process typically manifest
15 themselves as bands that are either being polished too quickly or too slowly. Conventional monitoring systems are not able to detect problem bands that lie totally within unmeasured bands (blind spots). This creates the very undesirable condition of having potential problem areas that are not detectable.

What is needed is a system for monitoring the front surface of a wafer during a
20 planarization process that is able to take frequent measurements with a plurality of probes, has a small spot size for higher sensitivity in detecting small residuals, has the ability to associate a measurement with its location on the wafer, takes measurements at various points on the surface of the wafer in a time period critical to and relevant to the particular process, and can determine areas on the wafer that need an increased or decreased material removal rate.

Summary of the Invention

The present invention is an apparatus and method for planarizing a front surface of a wafer. The invention includes a light source, such as a laser or xenon flash lamp, for generating a light signal. The light source is preferably capable of rapidly emitting a light signal of short
30 duration having multiple frequencies. The light signal may be communicated through fiber optic cables to a plurality of probes positioned adjacent the wafer. The light signal may be routed to one or more probes at a time.

The probes are preferably able to focus the light signal into a spot having a small size to

increase the sensitivity of the system. Each light signal is transmitted through a transparent area in the polishing pad (such as a window or hole) and is reflected off the front surface of the wafer and captured by a probe. The reflected signal may be communicated back from the probe through a fiber optic cable to a metrology instrument. The preferred metrology instrument is a
5 grating spectrometer capable of handling light signals from a plurality of sources. The spectrometer converts the light signal into data representing the intensity of the reflected light at various wavelengths.

The data may be transmitted to a control system, such as a general-purpose computer or network of computers used to automate the CMP tool, to analyze the data. The control system
10 may also gather data from other parts of the CMP tool to determine the location of each measurement on the front surface of the wafer. For example, the location of the measurement on the wafer may be found by using encoders on a carrier and a supporting surface. Another example is to use computer modeling of the various motions of the CMP tool to determine the probes location in relation to the wafer at any given time. With a plurality of measurements
15 from across the front surface of the wafer, the control system may determine if areas or bands are developing that require faster or slower material removal.

Orbital CMP tools, due to their generally small orbital radius in comparison to the size of the wafer, are advantageous in that they allow a majority of the probes to continuously remain under the wafer during the planarization process. In addition, due to the typical motions
20 in an orbital CMP tool, each probe tends to take all its measurements within a single concentric band on the front surface of the wafer simplifying the analysis. The bands may be allowed to overlap to insure the entire front surface of the wafer is being monitored and to enable health monitoring of the various probes. The measurements for each concentric band are easily identified in an orbital CMP tool as they tend to come from the same probe. Other types of
25 CMP tools may also be used, but they will generally receive data for a single band from different probes increasing the difficulty of the analysis process.

The control system may then alter a planarization parameter, such as the pressure in one or more zones in a multizone carrier, to improve the planarization process. Alternatively, or in addition, the control system may use the results from the current wafer to alter one or more
30 planarization parameters for future incoming wafers.

Brief Description of the Drawings

The present invention will hereinafter be described in conjunction with the appended

drawing figures, wherein like numerals denote like elements, and:

Figure 1 is a greatly expanded cross section view of a semiconductor chip;

Figure 2 is a greatly expanded cross section view of an interconnect in a semiconductor chip;

Figure 3 is a simplified cross section view of an apparatus used to practice the present

5 invention;

Figure 4 is a top plan view of Figure 3;

Figure 5 is a cross section view of an apparatus for producing an orbital motion; and

Figure 6 is a flow diagram of an exemplary process for practicing the present invention.

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Detailed Description of Exemplary Embodiments

An improved polishing apparatus and method utilized in the polishing of semiconductor wafers and thin films formed thereon will now be described. In the following description, numerous specific details are set forth illustrating Applicant's best mode for practicing the present invention and enabling one of ordinary skill in the art to make and use the present
15 invention. It will be known, however, to one skilled in the art that the present invention may be practiced without these specific details. In other instances, well-known machines and process steps have not been described in particular detail in order to avoid unnecessarily obscuring the present invention.

An apparatus for practicing the present invention will now be discussed with reference
20 to Fig. 3. During a planarization process, a wafer 100 may be transported by a carrier 301 to a position adjacent and substantially parallel to a working surface or polishing pad 309. The front surface of the wafer 100 is pressed against the polishing pad 309 fixed to a supporting surface 211, preferably in the presence of a slurry (not shown). The front surface of the wafer 100 is planarized by generating relative motion between the front surface of the wafer 100 and the
25 polishing pad 309 thereby removing material from the front surface of the wafer 100.

The invention includes a plurality of probes 305a-c positioned beneath the polishing pad 309 to transmit light to, and receive reflected light from, the front surface of the wafer 100. Three probes 305a-c are illustrated in Fig. 3, however, any number of probes may be used. The greater the number of probes, the faster a complete scan of the wafer may generally be taken,
30 but each additional probe increases the expense and complexity of the system. The probes 305a-c are preferably bifurcated to allow separate optical paths for the transmitted and reflected light. A light source 317 creates the light signal and a spectrometer 318 may be used to assist in analyzing the reflected light. Each probe 305a-c may be positioned to transmit light to, and

receive reflected light from, a particular annular band on the front surface of the wafer 100. If an orbital CMP tool is used having a relatively small orbital radius, each probe 305a-c may be used to monitor a single annular band. The annular bands in such an orbital CMP tool may be made to overlap to insure the entire front surface of the wafer 100 is being monitored. Analysis of the reflected light may then be used to alter the planarization process. For example, a planarization process parameter may be adjusted or the pressure exerted on the back surface of the wafer 100 may be altered by a multizone carrier 301.

The carrier 301 is preferably rotated about its central axis as it presses the front surface of the wafer 100 against the polishing pad 309 during the planarization process. The rotational speed of the carrier 301 is preferably selected to optimize the planarization process. The optimum rotational speed for the planarization process may be determined through computer models or by empirical means. Rotational speeds of about 12 rpm for the carrier 301 have been found to produce satisfactory planarization results while permitting the transmittance and reception of reflected light from the front surface of the wafer 100. The carrier 301 may also be moved along the polishing pad 309 to enhance the planarization process of the wafer 100.

The carrier 301 may be adapted to permit biasing the pressure exerted on different areas of the back surface of the wafer 100. Areas on the back surface of the wafer 100 receiving a higher (or lower) pressure will typically increase (or decrease) the removal rate of material from corresponding areas on the front surface of the wafer 100. Removal rates of material from planarization processes are typically substantially uniform within concentric annular bands about the center of the wafer, but often differ greatly from band to band. To correct for this common problem, the carrier 301 is preferably capable of exerting different pressures in a plurality of different areas while maintaining a uniform pressure within each area. Since removal rates for planarization processes tend to be uniform within concentric bands on the front surface of the wafer 100, the carrier 301 is ideally able to apply a uniform pressure over each concentric band on the back surface of the wafer 100. In addition, since removal rates tend to differ from band to band on the front surface of the wafer 100, the carrier 301 is also ideally able to apply different pressures over different bands on the back surface of the wafer 100. Examples of such carriers are disclosed in U.S. Patent No. 5,882,243; U.S. Patent No. 5,916,016; U.S. Patent No. 5,941,758; and U.S. Patent No. 5,964,653 and are hereby incorporated by reference. The greater the number of concentric annular bands, the greater the process flexibility in adjusting the carrier 301 to the problems encountered in the planarization process. However, the complexity and cost of the carrier also increases as the number of

adjustable bands increases. A carrier with three (3) adjustable concentric pressure bands is expected to give improved process flexibility while keeping the complexity of the carrier to a manageable level. Since the need for improved process results is almost certainly going to increase in the future, the preferred number of controllable bands within the carrier will also likely increase in the future.

A simplified view of one possible multizone carrier 301 that may be used with the invention is illustrated in Fig. 3. This illustrated carrier 301 has three concentric plenums: a central 303a, intermediate 303b and peripheral 303c plenum. A flexible membrane 314 provides a surface for supporting the wafer 100 while an inner 315 and an outer 316 ring provide barriers for separating the plenums 303a-c. The pressure within the central 303a, intermediate 303b and peripheral 303c plenums may be individually communicated through passageways 304a-c by respective controllable pressure regulators 313a-c connected to a pump 312. A rotary union 302 may be used in communicating the pressure from the pump 312 and pressure regulators 313a-c to their respective plenums 303a-c if the carrier 301 is rotated. A controller 311 may be used to automate the selected pressure for each pressure regulator 313a-c. Thus, each concentric plenum 303a-c may be individually pressurized to create three concentric bands to press against the back surface of the wafer 100. Each plenum 303a-c may therefore have a different pressure, but each concentric band will therefore have a uniform pressure within the band to press against the back surface of the wafer 100.

The invention may be used with a variety of polishing pads 309 that typically comprise a urethane-based material or other abrasive surfaces. Examples of conventional polishing pads 309 that may be used with the invention are an IC1000 or an IC1000 supported by a Suba IV polishing pad. Both of these polishing pads 309 and others are manufactured and made commercially available by Rodel, Inc. with offices in Phoenix, Arizona.

A supporting surface 211 may be used to support the abrasive surface or polishing pad 309. The supporting surface 211 may be a rigid substantially planar surface comprising aluminum, stainless steel, ceramic, titanium, polymer or other such rigid, non-corrosive material. Alternatively, the supporting surface 211 may comprise a diaphragm supported by pressurized fluids alone or in combination with other flexible layers. Both methods are known in the art for providing a supporting surface 211 for a polishing pad 309. A slurry delivery system (not shown) is preferably incorporated into the supporting surface 211 for delivery of slurry onto the polishing pad 309.

The supporting surface 211 may be connected to a motion generator 500 for creating

relative motion between the front surface of the wafer 100 and the polishing pad 309. Various motions for the supporting surface 211 are already known. For example U.S. Patent No. 5,498,196 shows an example of a rotational CMP tool; U.S. Patent No. 5,692,947 shows an example of a linear belt system; U.S. Patent No. 5,707,274 shows an example of a rotary drum system; and U.S. Patent No. 5,554,064 shows an example of an orbital tool, all of which are hereby incorporated by reference. However, the supporting surface 211 is preferably moved in an orbital motion in a manner more fully explained below in the description of the motion generator 500.

U.S. Patent No. 5,582,534 Shendon et al. and U.S. Patent No. 5,938,884 Hoshizaki et al. disclose several mechanisms for creating an orbital motion for a carrier. The principles for the mechanisms disclosed for creating an orbital motion may be applied by one of ordinary skill in the art to create a motion generator 500 capable of orbiting the supporting surface 211 and polishing pad 309 and are hereby incorporated by reference.

Fig. 5 is a cross-sectional view of an exemplary motion generator 500 that may be used to generate an orbital motion for the supporting surface 211. The motion generator 500 is generally disclosed in U.S. Patent No. 5,554,064 Breivogel et al. and is hereby incorporated by reference. Supporting base 220 may have a rigid frame 502 that can be securely fixed to the ground. Stationary frame 502 is used to support and balance motion generator 500. The outside ring 504 of a lower bearing 506 is rigidly fixed by clamps to stationary frame 502. Stationary frame 502 prevents outside ring 504 of lower bearing 506 from rotating. Wave generator 508 formed of a circular, hollow rigid stainless steel body is clamped to the inside ring 510 of lower bearing 506. Wave generator 508 is also clamped to outside ring 512 of an upper bearing 514. Wave generator 508 positions upper bearing 514 parallel to lower bearing 506. Wave generator 508 offsets the center axis 515 of upper bearing 514 from the center axis 517 of lower bearing 506. A circular supporting surface 211 is symmetrically positioned and securely fastened to the inner ring 519 of upper bearing 514. A polishing pad or pad assembly can be securely fastened to ridge 525 formed around the outside edge of the upper surface of the supporting surface 211. A universal joint 518 having two pivot points 520a and 520b is securely fastened to stationary frame 502 and to the bottom surface of the supporting surface 211. The lower portion of wave generator 508 is rigidly connected to a hollow and cylindrical drive spool 522 that in turn is connected to a hollow and cylindrical drive pulley 523. Drive pulley 523 is coupled by a belt 524 to a motor 526. Motor 526 may be a variable speed, three phase, two horsepower AC motor.

The orbital motion of supporting surface 211 is generated by spinning wave generator 508. Wave generator 508 is rotated by variable speed motor 526. As wave generator 508 rotates, the center axis 515 of upper bearing 514 orbits about the center axis 517 of lower bearing 506. The radius of the orbit of the upper bearing 517 is equal to the offset 527 between the center axis 515 of upper bearing 514 and the center axis 517 of the lower bearing 506. Upper bearing 514 orbits about the center axis 517 of lower bearing 506 at a rate equal to the rotation of wave generator 508. It is to be noted that the outer ring 512 of upper bearing 514 not only orbits but also rotates (spins) as wave generator 508 rotates. The function of universal joint 518 is to prevent torque from rotating or spinning the supporting surface 211. The dual pivot points 520a and 520b of universal joint 518 allow the supporting surface 211 to move in all directions except a rotational direction. By connecting the supporting surface 211 to the inner ring 519 of upper bearing 514 and by connecting universal joint 518 to the supporting surface 211 and stationary frame 502 the rotational movement of inner ring 519 and supporting surface 211 is prevented and supporting surface 211 only orbits as desired. The orbit rate of supporting surface 211 is equal to the rotation rate of wave generator 508 and the orbit radius of supporting surface 211 is equal to the offset of the center 515 of upper bearing 514 from the center 517 of lower bearing 506. The orbit rate of the supporting surface is preferably about 600 orbits per minute with a preferred orbit radius of less than 16 mm.

It is to be appreciated that a variety of other well-known means may be employed to facilitate the orbital motion of the polishing pad 309. While a particular method for producing an orbital motion has been given in detail, the present invention may be practiced using a variety of techniques for orbiting the polishing pad 309 on the supporting surface 211.

A multiprobe metrology instrument, e.g. a multiprobe end-point detection system 308, may be used to analyze data from the front surface of a wafer 100. Multiple probes allow samples to be taken at a desired density across the face of the wafer 100 in a shorter time than a single probe system, but increase the complexity of the system. This is accomplished since additional probes prevent or shorten the time when there are no probes under the front surface of the wafer 100 and may even allow multiple points to be sampled substantially simultaneously. It is highly desirable to take samples at a desired spatial density across the entire surface of the wafer 100 (a "scan") as quickly as possible to obtain the best possible data to analyze. The surface of the wafer 100 changes rapidly during the planarization process and a long interval between samples will result in the early measurements not accurately reflecting the condition of the wafer 100 when the later measurements are taken. Interpolation,

extrapolation or modeling software may be used to make estimates that compensate for temporal variations in samples, but the preferred method is to complete the scan as quickly as possible.

As a specific example, a short scan time may help avoid dishing or erosion in a copper planarization process. Once an area has cleared (copper has been removed down to a barrier layer) a scan of the entire wafer is preferably completed with the desired resolution within the time necessary to prevent excessive erosion or dishing of features in the cleared area. Measurements are most important once an area has cleared to make sure the planarization process stops before that area or any other area experiences erosion or over-polishing.

The multiprobe end-point detection system 308 may be used to determine areas on the front surface of the wafer 100 that need an increase or decrease in material removal rate. The areas that need an increase or decrease in material removal rate will typically take the form of concentric rings about an annular central region on the front surface of the wafer 100.

A multiprobe end-point detection system 308 is advantageous in CMP tools where a wafer does not remain over the same area of a polishing pad, as in a conventional rotational CMP tool. Multiple probes may be used to reduce the amount of time when no probe is under the wafer or may be used to increase the number of points sampled when more than one probe is under the wafer. A multiprobe end-point detection system 308 is also advantageous in systems where the wafer remains substantially over the same area of a polishing pad, as in a conventional orbital system. As illustrated in Fig. 3, the probes 306a-c may be positioned where they are always, or almost always, under the wafer 100 thereby allowing multiple probes 306a-c to take samples substantially continuously. This highly efficient use of the probes 306a-c greatly reduces the time necessary to complete a scan and greatly increases the accuracy of the analysis of the front surface of the wafer 100 by limiting temporal differences in the samples.

Referring still to Fig. 3, an emitter or flash lamp 317 may be used to initiate a light signal to travel through one or more fiber optic cables 307a-c. One or more probes 306a-c may be used to receive and transmit the light signal through one or more holes or transparent areas 305a-c in the polishing pad 309 to the front surface of the wafer 100. The probes 306a-c are preferably positioned as close as possible to a transparent area 305a-c to enhance the optical communication. The reflected light signal from the wafer 100 may be captured by a probe 306a-c and routed to a metrology instrument 318, such as a spectrometer, via fiber optic cables 307a-c. The invention may be practiced with a variety of probes, flash lamps and fiber optic

cables that are known in the art.

While measurement averaging or integration over a large area may be used to collect samples, a flash lamp 317 allows high-speed discrete measurements to be taken. Discrete measurements provide finer spatial resolution and are capable of detecting smaller residuals on the front surface of the wafer 100. The light signal is preferably a broad band spectrum of light so that the intensity of the reflected light signal may be analyzed at multiple wavelengths. The spectrum preferably includes light between 300 and 800 nm in wavelength. As a specific example, a Xe flash lamp 317 may be used to generate the light signal.

The flash duration of the flash lamp 317 should be as short as possible to minimize the amount of relative motion between the surface of the wafer and the flash lamp 317 and probe 306a-c during signal collection. Relative motion between the surface of the wafer 100 and the probe will create a smear effect and decrease the sensitivity of the measurement if the illumination were to endure over the relatively large duty cycle period of a grating spectrometer. The flash duration does need to be long and intense enough, however, to provide enough signal intensity for the probes 306a-c to collect the reflected light from the surface of the wafer 100. The flash duration is preferably less than about three microseconds.

The flash is optimally repeated as quickly as possible in order to gather the greatest amount of sample data. However, two factors limit the usefulness of extremely fast flash rates. The first is that each flash provides a tremendous amount of data that must be quickly analyzed. Data that has been gathered, but that cannot be timely analyzed does not benefit the system. The second factor is that some time must be allowed to pass between measurements in order for relative motion between the front surface of the wafer 100 and the probes 306a-c to move the measurement location. The measurements are preferably evenly distributed, and as close as possible, across the front surface of the wafer 100.

The spot size of light from the flash lamp 317 is preferably slightly larger than the largest copper feature that is supposed to remain on the surface of the wafer 100. This will prevent a fully planarized area from giving a false reading indicating that copper residuals remain. This could happen if a measurement were taken over a large copper feature, such as a bond pad, with a spot size smaller than the bond pad. On the other hand, a spot size that is too big may miss copper residuals that are smaller than the spot size. The optimum spot size is larger than the largest copper feature while also being smaller than the smallest copper residual it is required to detect. As feature sizes continue to decrease and the requirements for semiconductor manufacturing continue to become more stringent, the optimum spot size will

decrease. A spot size of one to three mm in diameter is acceptable for most current semiconductor manufacturing requirements with smaller spot sizes likely required in the future.

There are preferably enough probes properly positioned in the CMP tool to allow sampling across the entire front surface of the wafer 100 during the planarization process.

5 Typical orbital CMP tools, due to the small relative movements between the front surface of the wafer 100 and the polishing pad 309, need multiple probes that preferably have a slight overlap of coverage to insure all areas on the front surface of the wafer 100 are sampled. Each probe in a conventional orbital CMP tool, with a rotating carrier 301, will examine an annular band on the front surface of the wafer 100 approximately the width of the diameter of the orbit. Thus, 10 all the data for a particular annular band on the front surface of the wafer 100 in a conventional orbital CMP tool comes from a single probe thereby simplifying the analysis of the data.

The metrology instrument 318, preferably a grating spectrometer(s), accepts the incoming reflected light signal and converts the light signal into data indicating the intensity of the reflected light at a plurality of different wavelengths. The data may then be transmitted to a 15 control system 311 for analysis. The control system 311 is able to determine the condition of the front surface of the wafer 100 from the data. For example, a transition from a copper to a silicon dioxide surface may easily be detected with a broadband probe. The reflected signal itself may have monotonic characteristics when reflected from a metallic layer and sinusoidal characteristics when reflected from an oxide layer (or a nonmetallic layer). Specifically, the 20 reflected signal may become sinusoidal in nature after the metallic layer has been cleared away and the oxide layer becomes the reflective surface.

A number of numerical methods may be used to determine when the planarization process should be terminated, i.e. end-point called. For example, end-point may be called after a predetermined over-polish time has occurred starting from the time a predetermined 25 percentage of measurements show an absence of copper. The over-polish time insures a complete clearing of the copper. The over-polish time and the percentage of measurements showing an absence of copper are preferably determined empirically due to variations from planarization process to planarization process.

Another method of analyzing the data compares the measurements taken with 30 previously determined polish-time curves. This allows the time remaining prior to calling end-point to be calculated. Each new wafer planarized may be used to update and further refine the polish-time curves.

Another method of analyzing the data compares the clearing time for different

concentric areas on the front surface of the wafer 100. This method may be simplified when each probe monitors a particular, possibly overlapping, concentric band as would be the case when this invention is used with orbital CMP tools with a relatively small orbital radius. For example, probes 306a-c below areas that clear first indicate bands that are being polished too quickly in comparison to other bands. Corrective action may then be taken for that wafer 100 or the information may be used to improve the planarization process for incoming wafers.

The control system 311 may make immediate adjustments to the planarization process based on the analysis of the measurements. For example, increasing or decreasing the pressure on the back surface of the wafer 100 during the planarization process has been found to increase or decrease, respectively, the removal rate at the periphery of the wafer 100 with respect to the center of the wafer 100. As another example, more or less slurry may be distributed near areas that have been found to need increased or decreased, respectively, removal rates. As yet another example, the rotation speed of the carrier 301 may be increased or decreased to increase or decrease, respectively, the removal rate at the periphery of the wafer 100. However, the preferred method is to use a multizone carrier 301 to alter the removal rate at different areas of the front surface of the wafer 100. Specifically, the pressure may be increased or decreased in zones over areas that need an increase or decrease in material removal rate, respectively, on the front surface of the wafer 100. In addition, the results from planarized wafers 100 may be used to change the process parameters for incoming wafers. This allows process drift within the planarization process to be detected and compensated for as it happens.

The probes, fiber optic cables, flash lamps and spectrometer that may be used in practicing the invention are well known and may be purchased from Verity Instruments, Inc. headquartered in Dallas, Texas.

To determine the condition of the front surface of the wafer 100, the location for each measurement should be known. One possible method is to track only the radial position for each measurement and take at least one measurement at various radial positions in fine enough increments to provide a desired sampling resolution. This method assumes that each measurement accurately represents the condition of the wafer 100 at every point having the same radial position. Since wafers 100 generally have bands that planarize at approximately the same rate, this method provides a simple approximation of the condition of the front surface of the wafer 100. However, this method may miss areas that do not follow the normal band pattern. The preferred method is to track the position of each measurement in two axes on the front surface of the wafer 100. In this manner, measurements may be taken across the front

surface of the wafer 100 at a desired spatial resolution that prevents a problem area larger than the desired resolution from going unobserved.

Various devices may be used to track the location of the measurement on the front surface of the wafer 100. For example, an encoder 320 may be used to track the position of the carrier 301 (and thus the wafer) and transmit this information via communication line 319 to the control system 311. In a similar manner, an encoder 321 may be used to track the position of the supporting surface 211 (and thus the probes) and transmit this information via communication line 322 to the control system 311. The wafer 100 may need to be firmly held in the carrier 301 to prevent the wafer 100 from spinning and randomizing its orientation. For example, the wafer 100 may be held in place by applying suction to the back surface of the wafer 100 through the membrane 314 or by creating a tacky bottom surface for the membrane 314. The control system 311 thus has the information necessary to match the data from the metrology instrument 318, preferably a spectrometer, with the data's corresponding location on the front surface of the wafer 100. Alternatively, modeling software for the mechanical mechanisms that cause the relative motion between the wafer 100 and the polishing pad 309 may also be used to predict the location of each measurement on the front surface of the wafer 100. Modeling software is also useful in determining desirable motions for the carrier 301 and supporting surface 211, and thus the wafer 100 and probes, that will produce a pattern of measurements as evenly distributed as possible. Small adjustments to the desired relative motion between the wafer 100 and the polishing pad 309 may be made to improve the distribution of measurements while having only a minimum impact on the planarization process. An evenly distributed pattern may shorten the time of a scan by requiring the minimum number of measurements and the least amount of data processing. However, the measurement locations do not have to be evenly distributed, but the largest space between measurements is preferably smaller than the targeted residual detection capability.

Alternatively, the measurements may be analyzed until the largest possible remaining residual is of a predetermined size. Once all the remaining residuals are of the predetermined size or smaller, the wafer 100 may be planarized for an additional time (over-polish time) to remove the remaining residuals. The additional planarization time may be found by empirically determining the maximum amount of time necessary to planarize away residuals of the predetermined size.

An illustrative method for planarizing a front surface of a wafer 100 will now be described with reference to Figs. 3 and 6. The wafer 100 is placed in a carrier 301 (step 600)

and transported adjacent a polishing pad 309. The carrier 301 holds the wafer 100 substantially parallel to the polishing pad 309 while the wafer 100 is pressed against the top surface of the polishing pad 309 (step 601). The carrier 301 may be rotated or otherwise moved in relation to the polishing pad 309 to assist in uniformly removing material from the front surface of the wafer 100. The supporting surface 211 and attached polishing pad 309 may also be moved in relation to the front surface of the wafer 100 and is preferably orbited. (step 602) The relative motion is necessary to remove material from the front surface of the wafer 100.

An end-point system 308 with two or more probes 306a-c may be used to take measurements across the front surface of the wafer 100 during the planarization process. The end-point system 308 may reflect a light signal off the front surface of the wafer 100 using a flash lamp 317. A spectrometer 318 may be used to convert the reflected light into data representing the intensity of the reflected light at a plurality of wavelengths. Linear encoders 320 and 321, computer modeling or other known methods for determining the physical location of the mechanical devices may be used to track the location of the carrier 301 and the supporting surface 211. This allows the location on the front surface of the wafer 100 to be determined for each measurement (step 603). A control system 311 may be used to analyze the measurements from the spectrometer 318 and the location of the measurements on the wafer 100 to determine the progress of the planarization process and the condition of the wafer 100 (step 604). If the wafer 100 has been sufficiently planarized, the planarization process may be terminated (step 605). The control system 311 may also be used to determine if an increased or decreased removal rate over a portion of the front surface of the wafer 100 is desirable (step 606). If the wafer 100 is being uniformly planarized, further measurements may be taken and analyzed (back to step 603). However, if the control system 311 determines the removal rate should be increased or decreased in particular areas, one or more planarization process parameters may be altered. For example, the down-force, slurry delivery profile, rotation speed of carrier, etc. may be adjusted to improve the planarization process. If a multizone carrier 301 is being used, an increased or decreased pressure may be exerted on the back surface of the wafer 100 opposite areas on the front surface of the wafer 100 that require an increased or decreased removal rate respectively (step 607). After altering the planarization process, further measurements may be taken and analyzed (back to step 603) with appropriate steps as described above taken. If the control system 311 determines the wafer 100 has been sufficiently planarized (step 605) the wafer may be unloaded from the carrier (step 608) and removed from the CMP tool. The planarization results for this wafer 100 may be used to determine an

improved planarization process for incoming wafers (step 609). This allows an improved down-force, slurry delivery profile, rotation speed of carrier, pressures within multizone carrier, relative motions between wafer 100 and polishing pad 309, etc. to be altered during the planarization process for incoming wafers to further improve the planarization process.

- 5 While the invention has been described with regard to specific embodiments, those skilled in the art will recognize that changes can be made in form and detail without departing from the spirit and scope of the invention.

Claims

We claim:

1. An apparatus used in monitoring the front surface of a wafer during a planarization
5 process, comprising:
 - a) a source for producing an interrogation signal;
 - b) a plurality of probes adapted to transmit the interrogation signal to, and receive reflected interrogation signal from, a front surface of a wafer;
 - c) a metrology instrument for measuring the interrogation signal; and
 - 10 d) a plurality of path ways for communicating the interrogation signal from the source to the probes and for communicating the reflected interrogation signal from the probes to the metrology instrument.
2. An apparatus used in monitoring the front surface of a wafer during a planarization
15 process, comprising:
 - a) a light source for producing a light signal;
 - b) a plurality of probes adapted to transmit the light signal to, and receive reflected light from, a front surface of a wafer;
 - c) a metrology instrument for measuring the intensity of the reflected light;
 - 20 and
 - d) a plurality of fiber optic cables for communicating the light signal from the light source to the probes and for communicating the reflected light from the probes to the metrology instrument.
- 25 3. The apparatus of claim 2, wherein the metrology instrument is a spectrometer.
4. The apparatus of claim 3, wherein the probes and spectrometer are adapted for working with multi-frequency light.
- 30 5. The apparatus of claim 2, further comprising:
 - e) a working surface for planarizing the front surface of the wafer;
 - f) a carrier for retaining and pressing the front surface of the wafer against the working surface;

- g) a rotational motion generator adapted for rotating the carrier; and
- h) an orbital motion generator adapted for orbiting the working surface.

6. The apparatus of claim 5, wherein the working surface is a polishing pad.

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7. The apparatus of claim 6, wherein the plurality of probes extend through the polishing pad.

8. The apparatus of claim 6, wherein the probes are positioned beneath the polishing pad to monitor a plurality of annular areas that substantially cover the front surface of the wafer.

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9. The apparatus of claim 6, wherein the probes transmit the light signal and receive the reflected light through the polishing pad.

10. The apparatus of claim 6, further comprising:

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- i) a plurality of windows in the polishing pad above the probes.

11. The apparatus of claim 6, wherein the polishing pad is transparent above the probes.

12. The apparatus of claim 8, wherein adjacent annular areas on the front surface of the wafer overlap.

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13. The apparatus of claim 8, wherein the carrier is adapted to place a first pressing force on a first annular area on the back surface of the wafer and a second pressing force on a second annular area on the back surface of the wafer.

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14. A method for planarizing a front surface of a wafer comprising the steps of:

- a) pressing a front surface of a wafer against a working surface;
- b) generating relative motion between the front surface of the wafer and the working surface;
- c) transmitting a plurality of light signals to a plurality of bands on the front surface of the wafer;
- d) receiving a plurality of reflected light signals from the plurality of bands with a plurality of probes;

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- e) communicating the reflected light from the plurality of probes to a metrology instrument;
- f) analyzing the plurality of reflected light signals from the plurality of bands; and
- 5 g) altering the planarization process based on the analysis.

15. The method of claim 14 wherein the planarization process is altered by adjusting the pressure in one or more zones of a multizone carrier.

10 16. The method of claim 14 wherein the plurality of bands on the front surface of the wafer overlap.

17. The method of claim 14 wherein the generated relative motion comprises rotating the carrier and orbiting the working surface.

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18. The method of claim 14 wherein the plurality of light signals comprise multiple frequencies.

19. The method of claim 14 wherein the metrology instrument comprises a spectrometer.

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20. An apparatus used in monitoring the front surface of a wafer and adjusting a planarization process, comprising:

- a) a source for producing an interrogation signal;
- b) a directing probe adapted to direct the interrogation signal to a front
- 25 surface of a wafer;
- c) a capturing probe adapted to capture the reflected interrogation signal from the front surface of the wafer;
- d) a metrology instrument in communication with the capturing probe;
- e) a control system in communication with the metrology instrument; and
- 30 f) a multizone carrier in communication with the control system.

21. The apparatus of claim 20 wherein the interrogation signal comprises multiple frequencies.

22. The apparatus of claim 20 wherein the interrogation signal comprises a single frequency.

23. The apparatus of claim 20 further comprising a plurality of directing probes and a plurality of capturing probes.

24. A method of monitoring and adjusting a planarization process, comprising the steps of:

a) planarizing a front surface of a wafer using an initial set of process parameters;

b) taking measurements at various positions across the front surface of the wafer while planarizing the wafer;

c) analyzing the measurements; and

d) altering a pressure within one or more zones of a multizone carrier based on the analysis of the measurements while planarizing the wafer.

25. The method of claim 24 further comprising the step of:

e) associating the measurements with locations on the front surface of the wafer.

26. The method of claim 25 wherein the measurements are associated with locations on the front surface of the wafer in two axes.

27. The method of claim 25 wherein the radial position of the measurements are associated with locations on the front surface of the wafer.

28. The method of claim 24 wherein the measurements are taken using an interrogation signal comprising multiple frequencies.

29. The method of claim 24 wherein the measurements are taken using an interrogation signal comprising a single frequency.

30. The method of claim 24 further comprising the step of:

e) altering the initial set of process parameters for subsequently planarized

wafers based on the analysis of the measurements.

31. The method of claim 24 wherein step c) comprises comparing a clearing time for a plurality of different areas on the front surface of the wafer.

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32. The method of claim 31 wherein the plurality of different areas comprises a plurality of concentric zones on the front surface of the wafer.

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33. The method of claim 24 wherein step c) comprises comparing an end point time for a plurality of different areas on the front surface of the wafer.

34. The method of claim 33 wherein the plurality of different areas comprises a plurality of concentric zones on the front surface of the wafer.

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35. A method for planarizing a front surface of a wafer, comprising the steps of:

a) pressing a front surface of a wafer within a multizone carrier against a working surface, wherein each zone in the multizone carrier is pressurized to a corresponding initial pressure;

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b) generating relative motion between the front surface of the wafer and the working surface;

c) transmitting a light signal to a front surface of the wafer;

d) receiving the reflected light signal from the front surface of the wafer;

e) communicating the reflected light signal to a metrology instrument;

f) converting the reflected light signal into data;

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g) communicating the data to a control system;

h) analyzing the data; and

i) altering the planarization process based on the analysis of the data.

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36. The method of claim 35 wherein step i) comprises altering the initial pressure within one or more zones of the multizone carrier.

37. The method of claim 36 wherein step h) comprises comparing a clearing time for a plurality of different areas on the front surface of the wafer.

38. The method of claim 37 wherein the plurality of different areas comprises a plurality of concentric zones on the front surface of the wafer.

5 39. The method of claim 35 wherein step h) comprises comparing an end point call time for a plurality of different areas on the front surface of the wafer.

40. The method of claim 39 wherein the plurality of different areas comprises a plurality of concentric zones on the front surface of the wafer.

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41. The method of claim 35 further comprising the step of:

j) altering the initial pressure within one or more zones of the multizone carrier based on the analysis of the data for subsequently planarized wafers.

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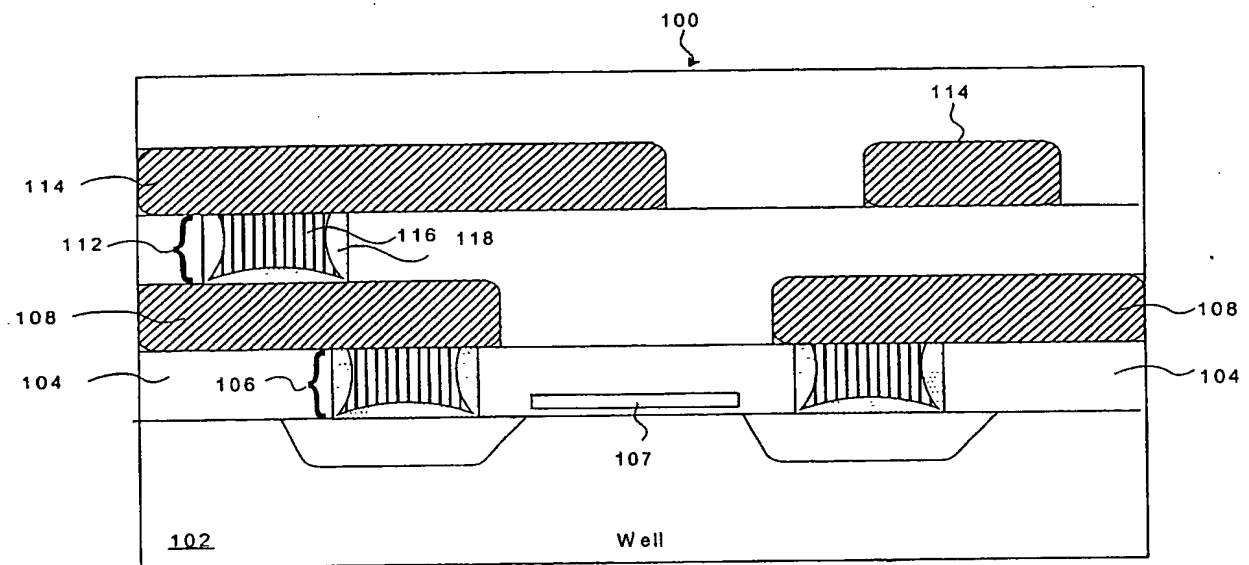


Fig. 1 (Prior Art)

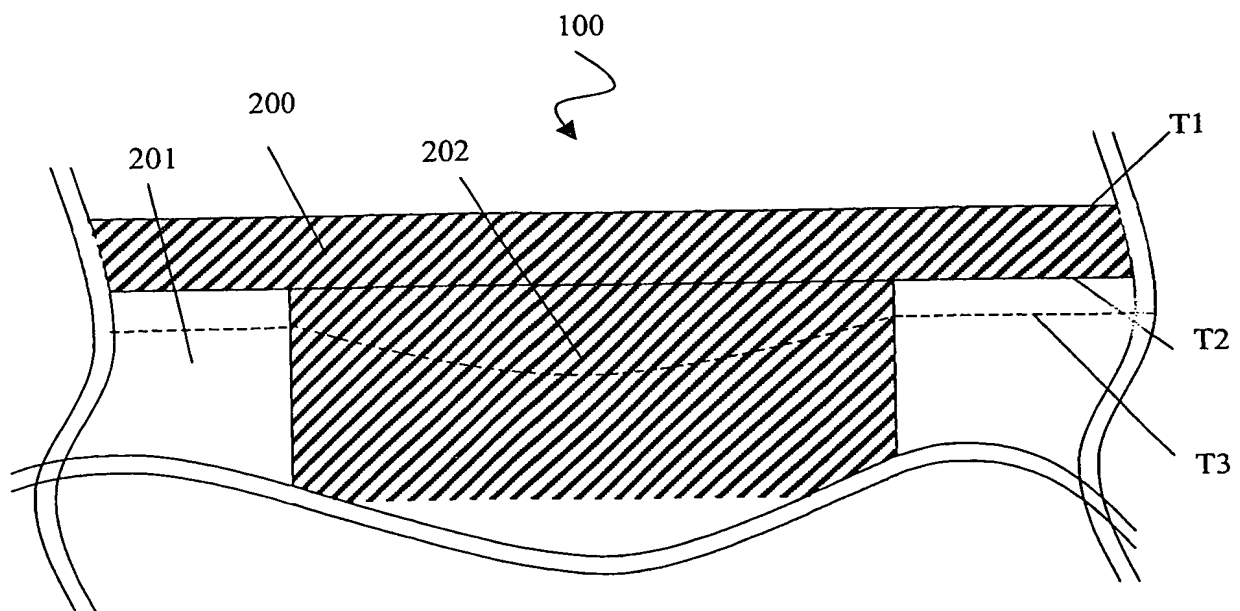
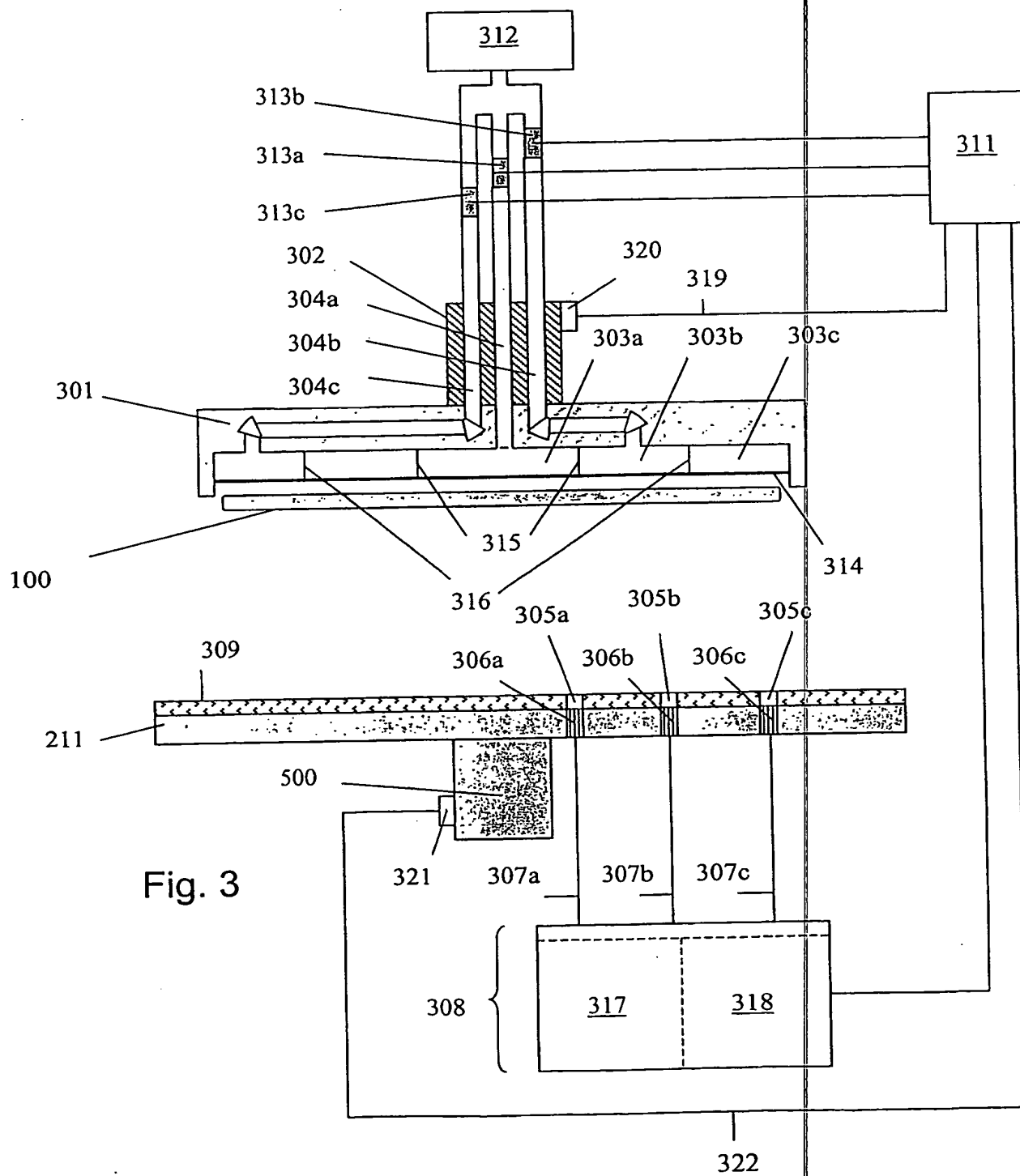


Fig. 2 (Prior Art)

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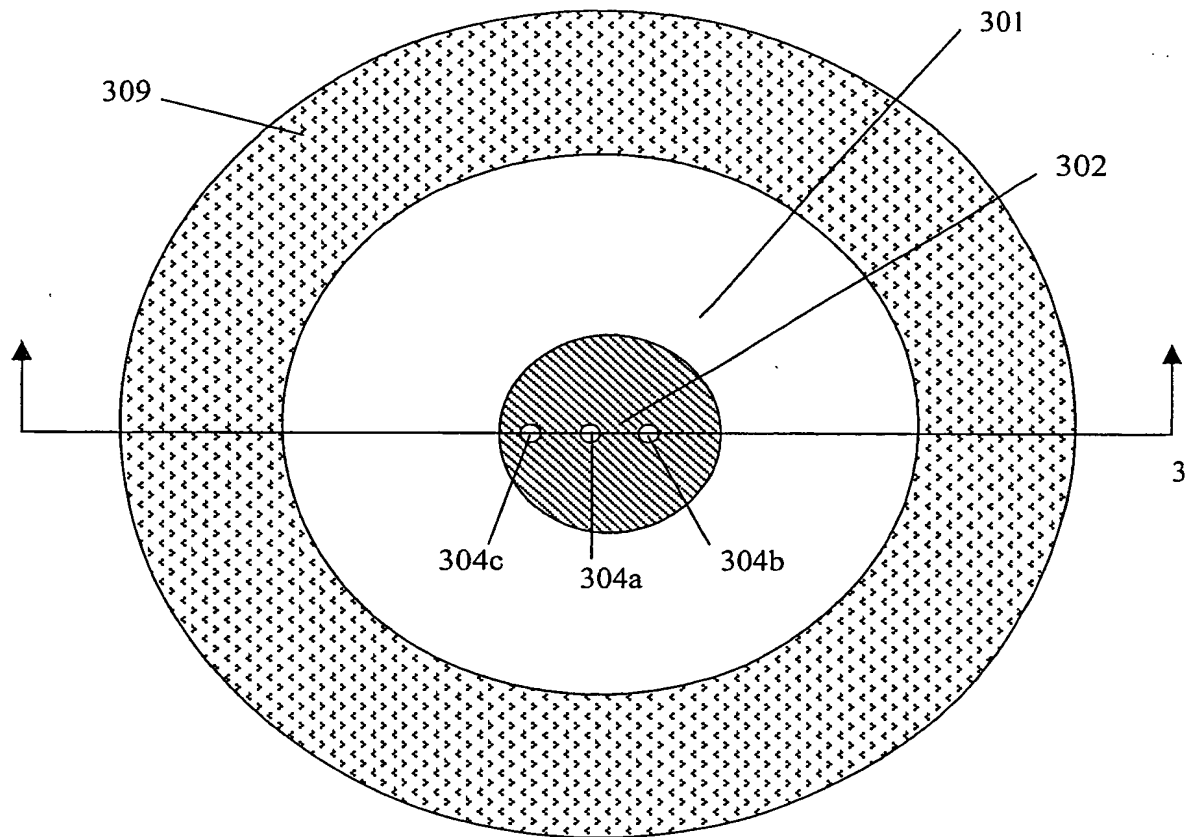


Fig. 4

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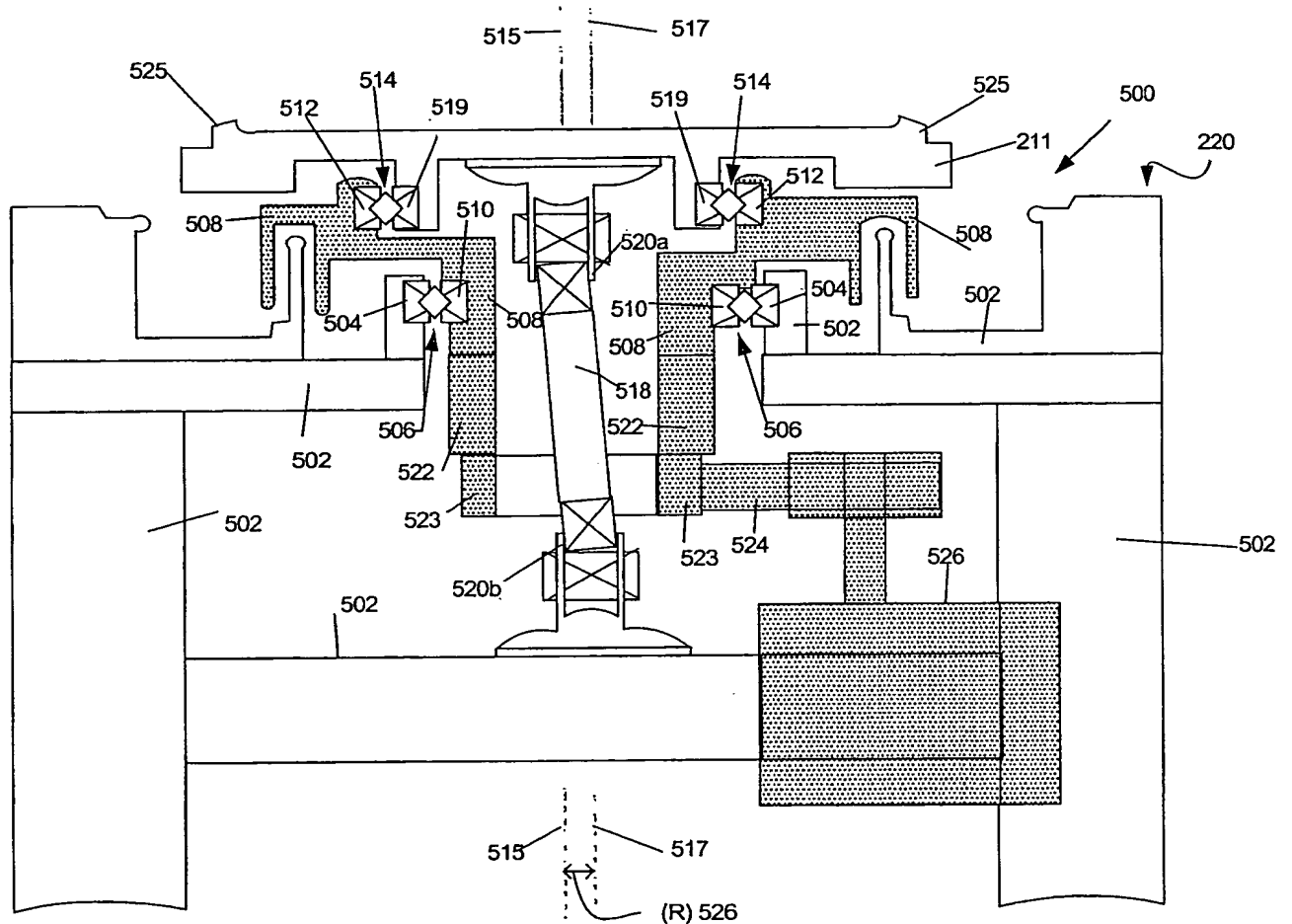


Fig. 5

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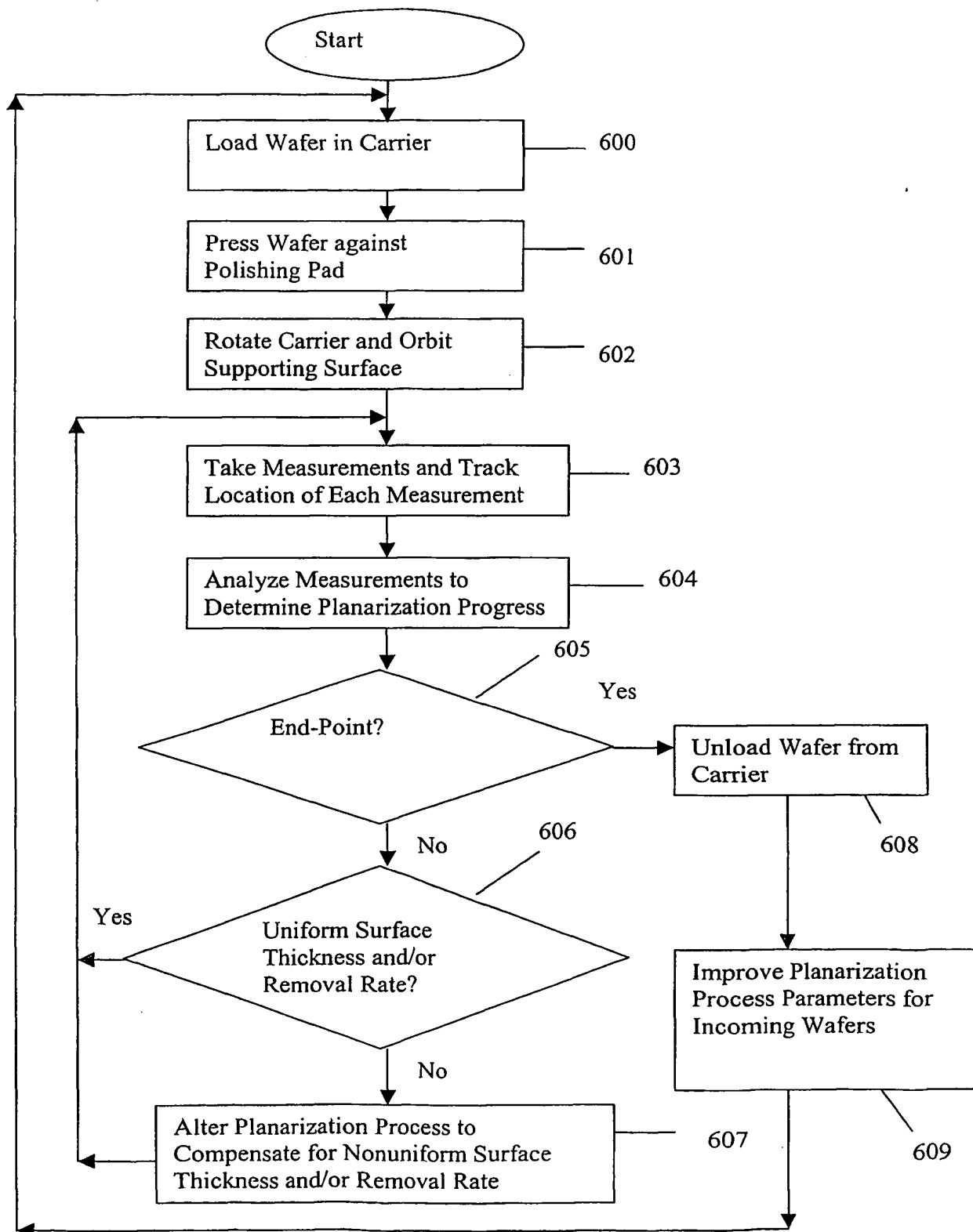


Fig. 6

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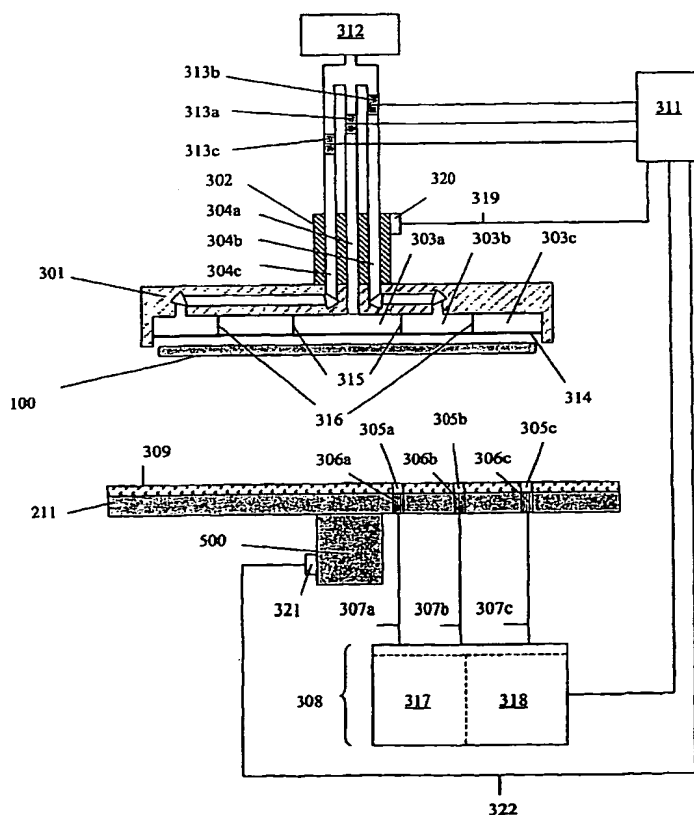
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(54) Title: MULTIPROBE DETECTION SYSTEM FOR CHEMICAL-MECHANICAL PLANARIZATION TOOL



(57) Abstract: The invention is a method and apparatus for planarizing a wafer (100). Discrete measurements are taken across the surface of the wafer (100) at a desired spatial density. The measurements may be generated using a flash lamp (317) to reflect a light signal off the surface of the wafer (100) with a spectrometer (318) analyzing the reflected light. A plurality of probes (306a-c) may be used at different locations to shorten the time necessary for taking measurements across the full front surface of the wafer (100) and for allowing a plurality of areas to be sampled substantially simultaneously. A control system receives the measurements and their corresponding locations. The control system (311) is then able to analyze the data looking for areas or bands on the front surface of the wafer (100) that need an increase or decrease in material removal rate. The control system (311) is then able to adjust one or more planarization parameters to improve the process for the current wafer or for future wafers.

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Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 872 633 A (HOLZAPFEL PAUL ET AL) 16 February 1999 (1999-02-16) column 5, line 65 -column 8, line 40	1-3,6, 19,22,35 14,20,24
A	US 6 071 177 A (LIN C L ET AL) 6 June 2000 (2000-06-06) abstract; figures	4,7-11, 18,21
A	US 6 095 904 A (BARNES CHRISTOPHER E ET AL) 1 August 2000 (2000-08-01) abstract	5,17
A	US 5 916 016 A (BOTHRA SUBHAS) 29 June 1999 (1999-06-29) cited in the application abstract; figures	13,15, 20,24,36

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Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 5872633	A	16-02-1999	DE 19882110 T0	11-05-2000
			GB 2337476 A ,B	24-11-1999
			JP 2000514251 T	24-10-2000
			TW 386276 B	01-04-2000
			TW 380079 B	21-01-2000
			WO 9805066 A2	05-02-1998
			WO 9834760 A1	13-08-1998
			US 6217410 B1	17-04-2001
			US 5958148 A	28-09-1999
			US 2001012108 A1	09-08-2001
US 6071177	A	06-06-2000	NONE	
US 6095904	A	01-08-2000	US 5554064 A	10-09-1996
			US 5876271 A	02-03-1999
US 5916016	A	29-06-1999	NONE	